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TRANSMITTAL LETTER
INFORMATION DISCLOSURE STATEMENT

Applicant : Ha et al.
App. No : 09/886,533
Filed : June 20, 2001
For : VIRTUAL HARDWARE MACHINE,
METHODS, AND DEVICES
Examiner : Chuck O. Kendall
Art Unit : 2122

CERTIFICATE OF MAILING

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

January 6, 2006

(Date)

Eric M. Nelson, Reg. No. 43,829

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application are:

- (X) An Information Disclosure Statement and PTO/SB/08 equivalent listing references for consideration:
 - (X) Listing five (5) references.
 - (X) Enclosing five (5) references.
- (X) A check in the amount of \$180 to cover the above fee is enclosed.
- (X) The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 11-1410.
- (X) Return prepaid postcard.

Eric M. Nelson
Registration No. 43,829
Attorney of Record
Customer No. 20,995
(619) 235-8550



Docket No.: IMEC214.001AUS

Customer No. 20,995



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Eric M. Nelson, Reg. No. 43,829

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application is a PTO/SB/08 Equivalent listing five references to be considered by the Examiner. Also enclosed are five foreign patent references and/or non-patent literature as listed on the Information Disclosure Statement.

This Information Disclosure Statement is being filed before the mailing date of a final action and before the mailing of a Notice of Allowance. This Statement is accompanied by the fees set forth in 37 C.F.R. § 1.17(p). The Commissioner is hereby authorized to charge any additional fees which may be required or to credit any overpayment to Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: 1/5/2006

By:

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT***(Multiple sheets used when necessary)*

SHEET 1 OF 1

Application No.	09/886,533
Filing Date	June 20, 2001
First Named Inventor	Ha et al.
Art Unit	2122
Examiner	Chuck O. Kendall
Attorney Docket No.	IMEC214.001AUS

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number Number - Kind Code (if known) Example: 1,234,567 B1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document Country Code-Number-Kind Code Example: JP 1234567 A1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear	T ¹
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ¹
	1	European International Search Report. Dated March 1, 2005.	
	2	Guccione, Steve, et al., "JBits: Java based interface for reconfigurable computing," presented at the 2 nd Annual Military Aerospace Applications of Programmable Devices and Technologies Conference, MAPLD 1999, Sept. 28-30, 1999.	
	3	HA et al., "A Scalable Architecture to Support Networked Reconfiguration," ProRISC 99. 10 19-01:101. 677-83, November 24, 1999.	
	4	Ha, Yajun, et al., "Building a Virtual Framework for Networked Reconfigurable Hardware and Software Objects," presented at International Conference on Parallel and Distributed Processing Techniques and Applications 2000, PDPTA 2000, June 24-29, 2000.	
	5	FLEISCHMANN et al., "A Hardware/Software Prototyping Environment for Dynamically Reconfigurable Embedded Systems." International Conference on Hardware Software Codesign, Seattle, Washington, Proc. Of the 6 th International Workshop on Hardware/Software codesign, pp.105-109, March 1998.	

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Examiner Signature

Date Considered

***Examiner:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

T¹ - Place a check mark in this area when an English language Translation is attached.